

FIG. 1

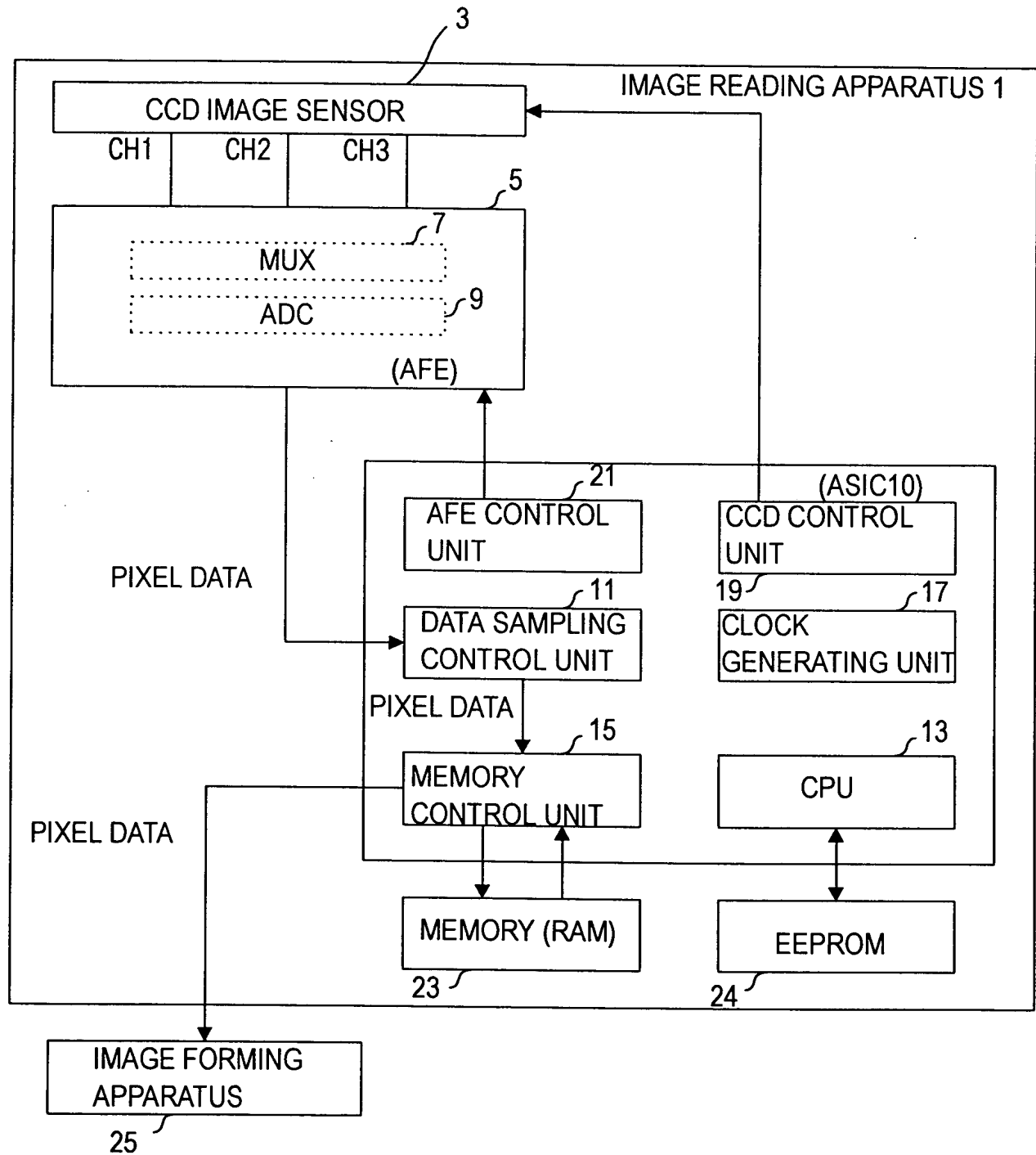


FIG. 2

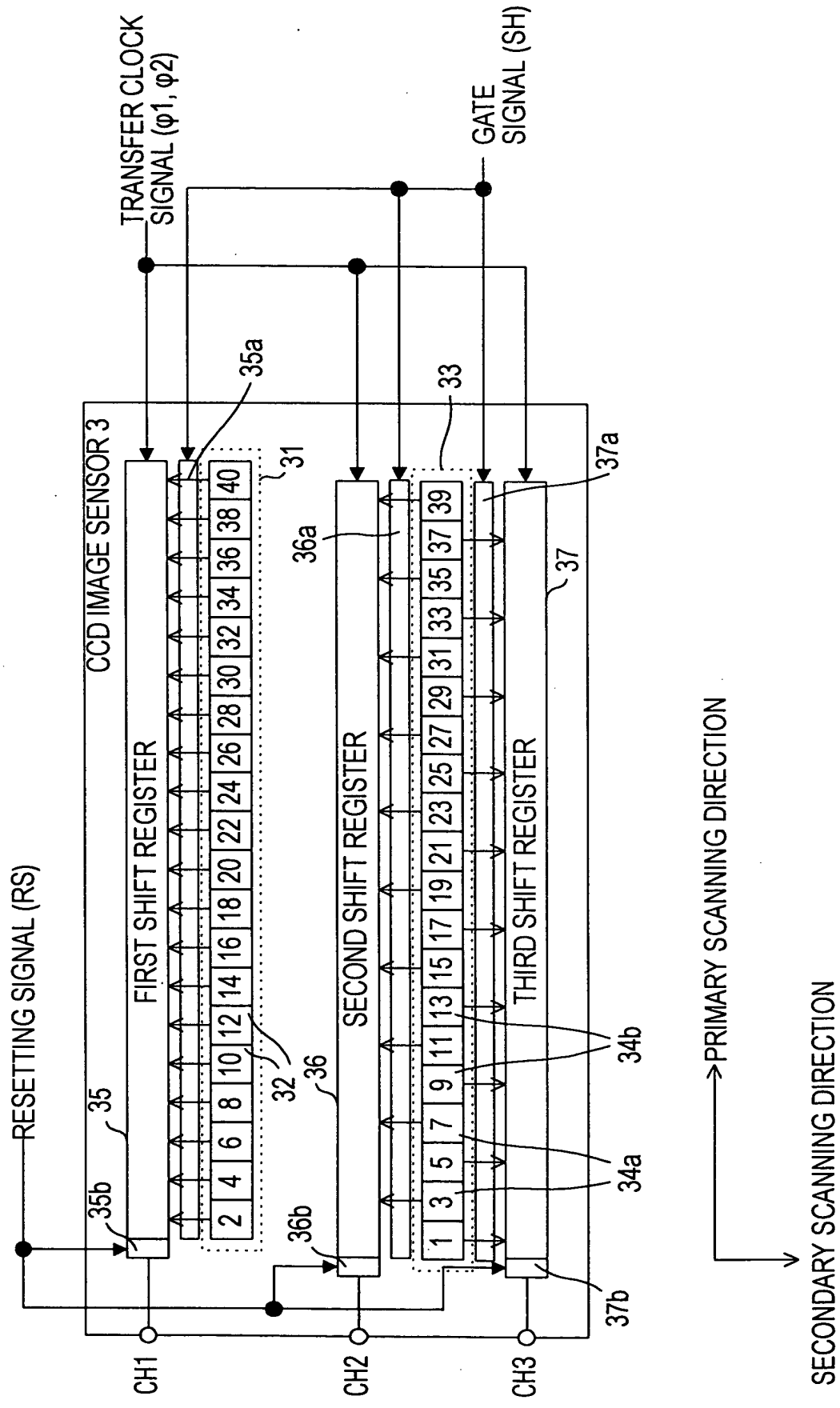


FIG. 3

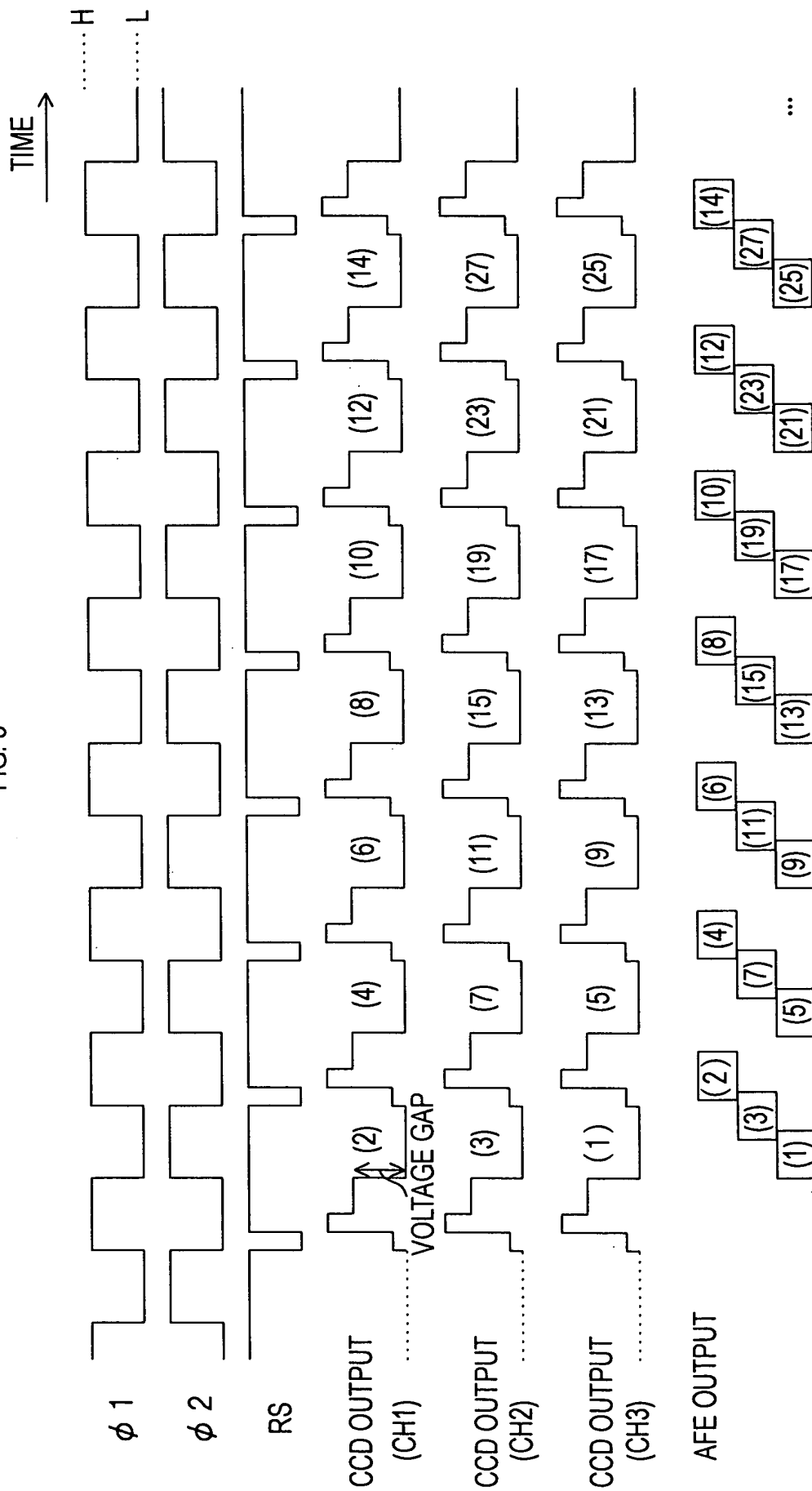


FIG. 4

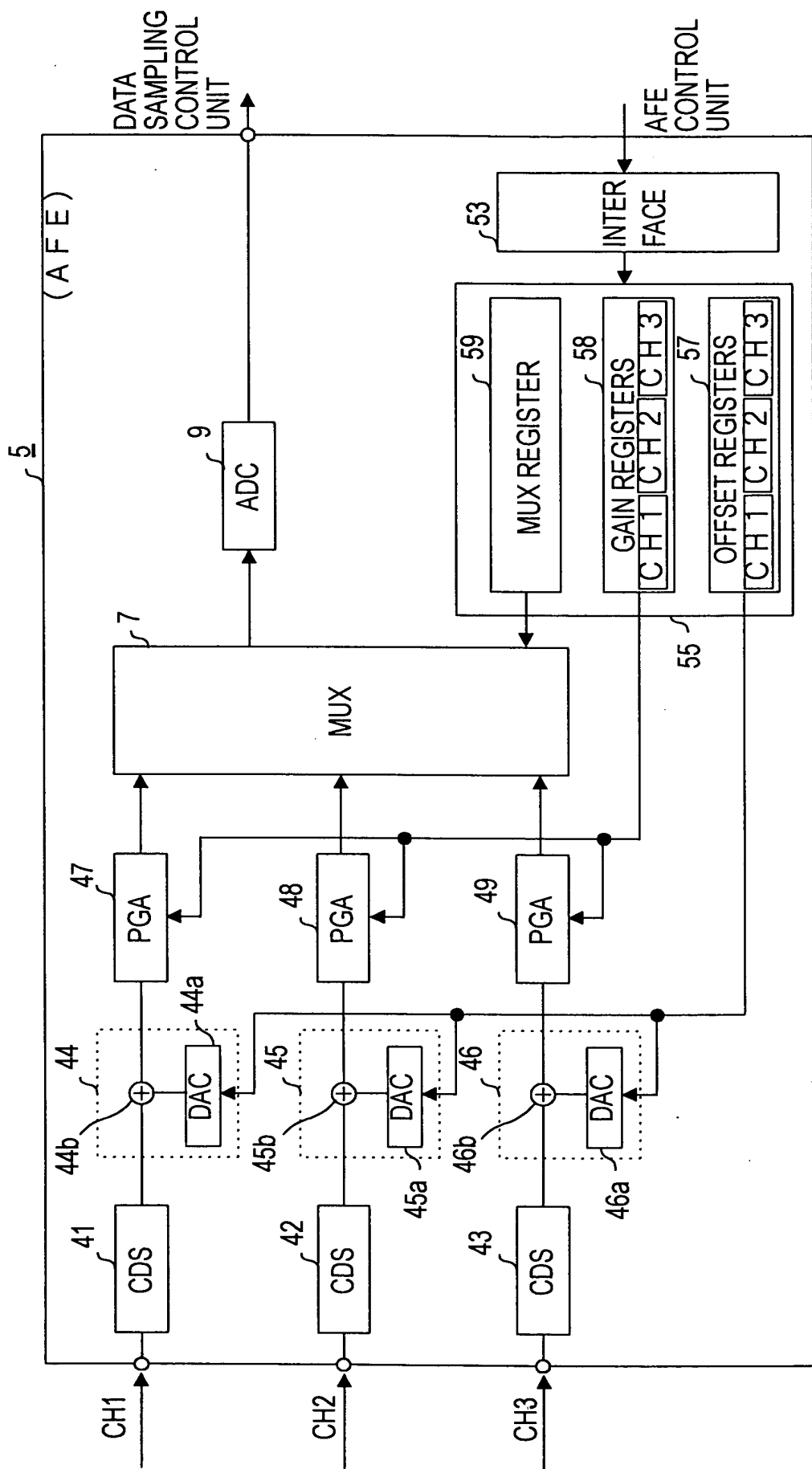


FIG. 5

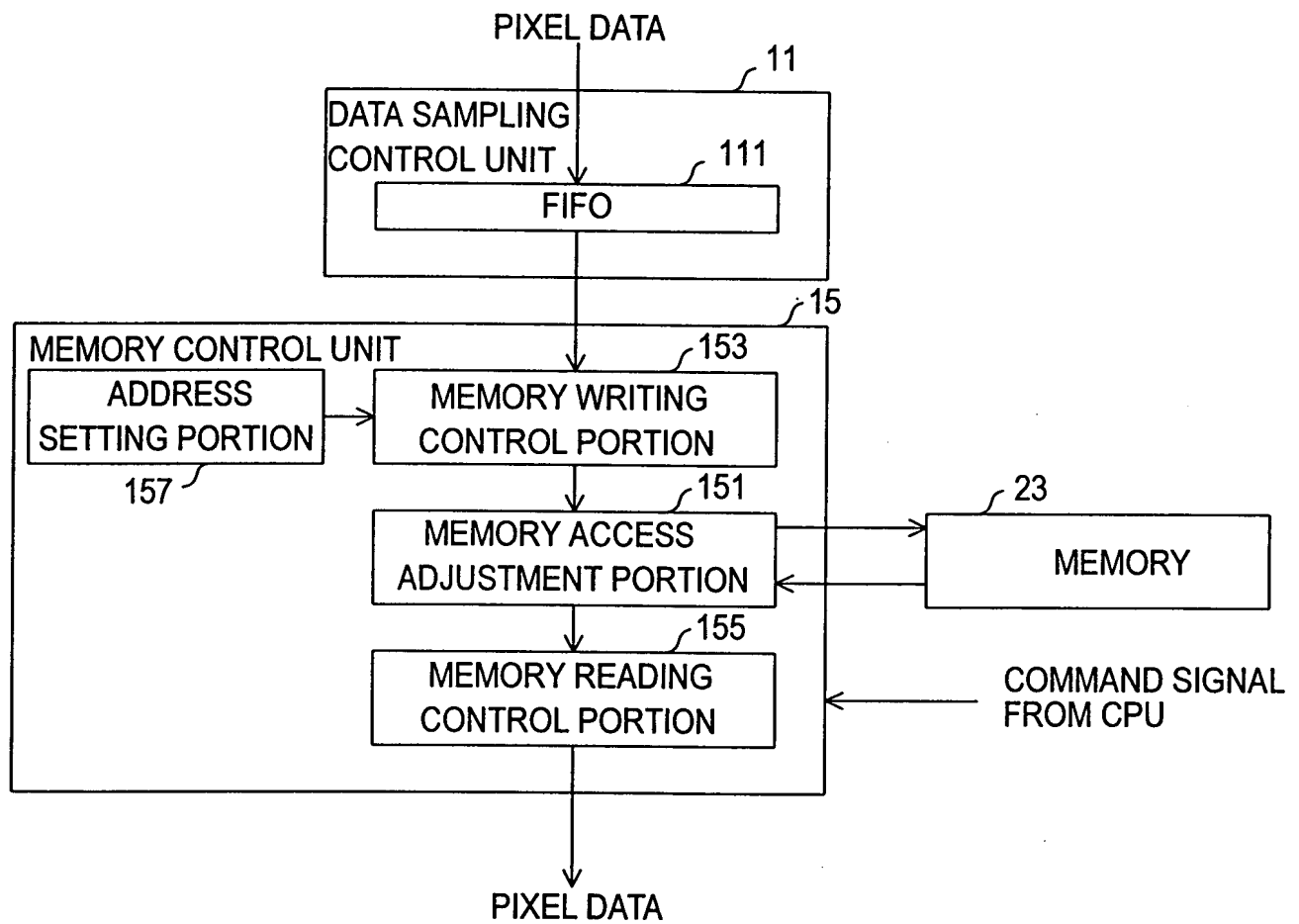


FIG. 6

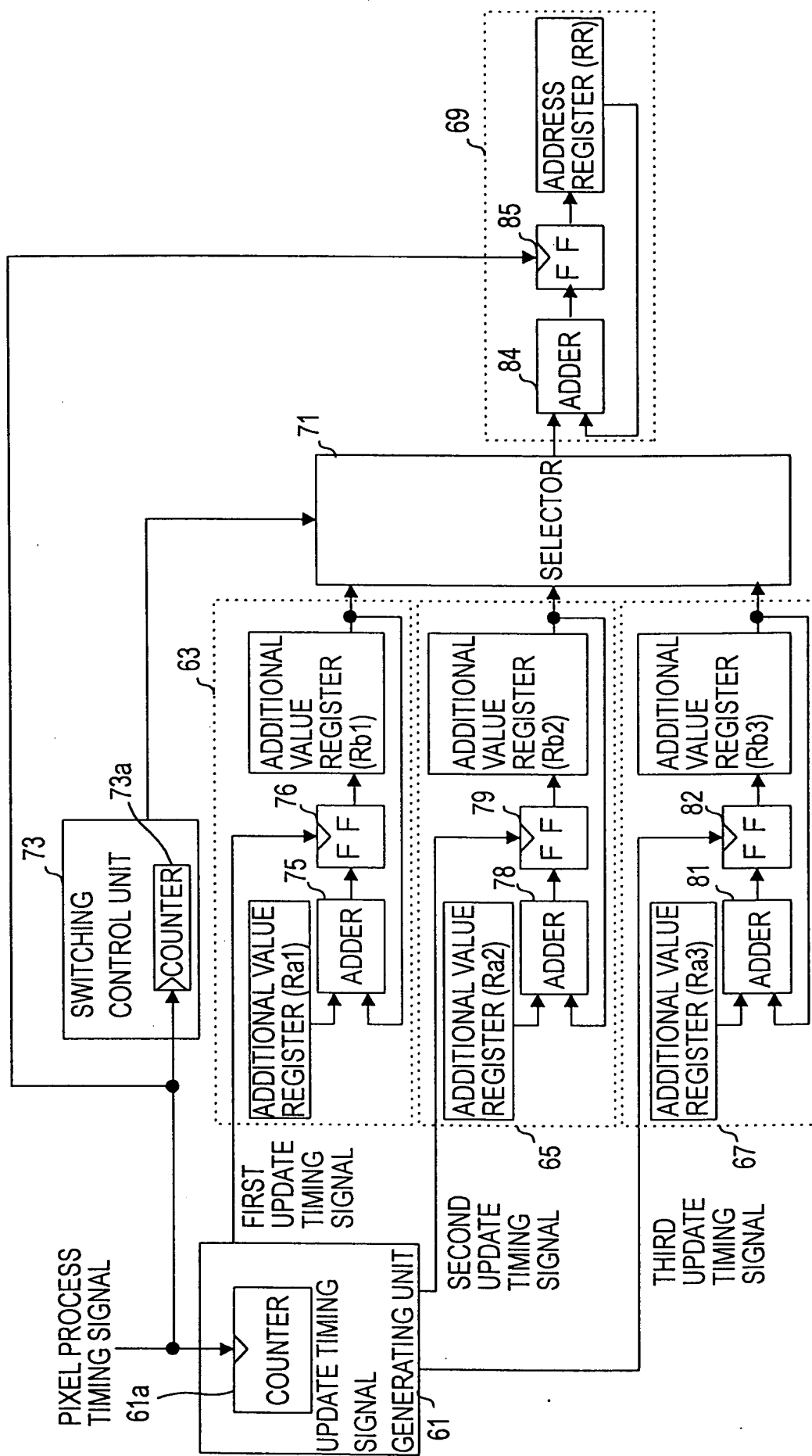


FIG. 7

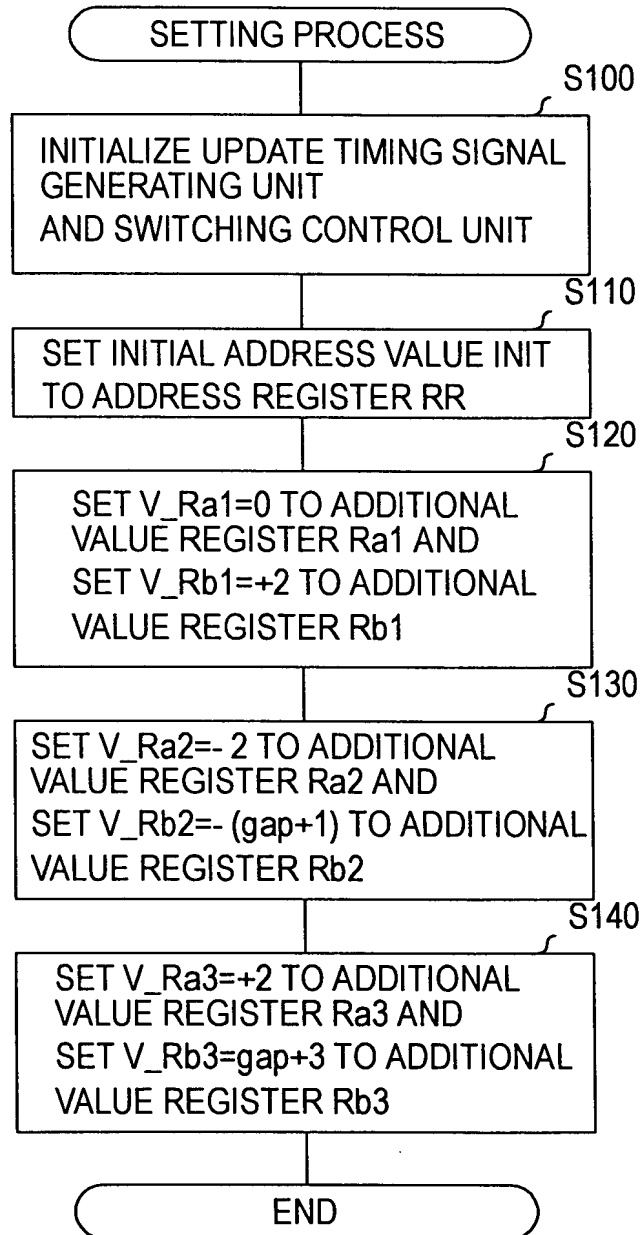


FIG. 8A

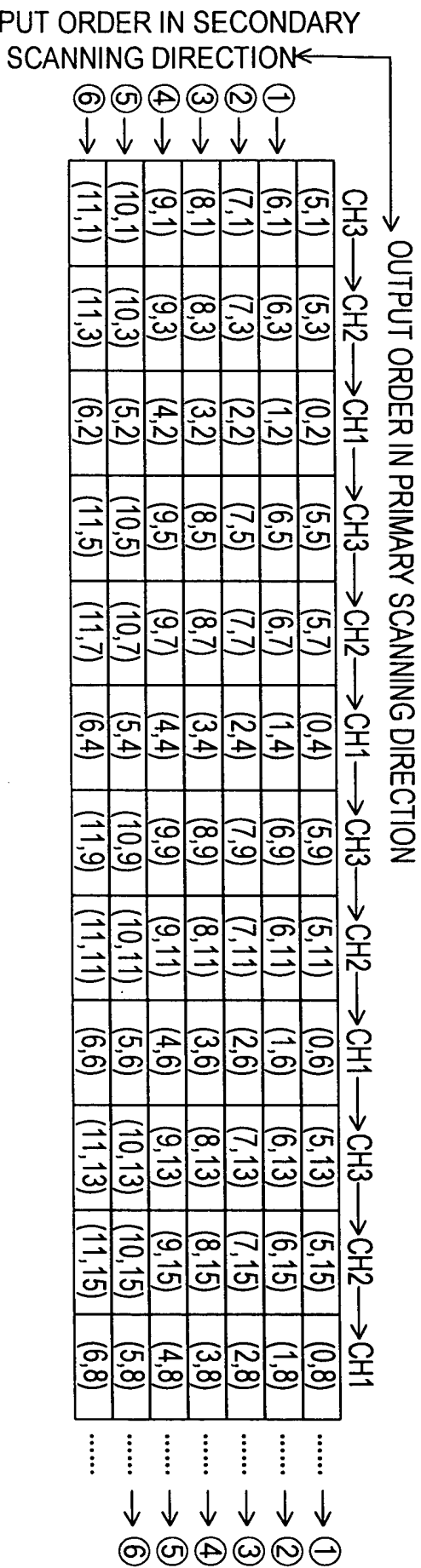


FIG. 8B

HIGHER ADDRESS

LOWER ADDRESS

	00	01	02	03	04	05	06	07	08	09	0a	0b
0x05	(5,1)	(5,2)	(5,3)	(5,4)	(5,5)	(5,6)	(5,7)	(5,8)	(5,9)	(5,10)	(5,11)	(5,12)
0x06	(6,1)	(6,2)	(6,3)	(6,4)	(6,5)	(6,6)	(6,7)	(6,8)	(6,9)	(6,10)	(6,11)	(6,12)
0x07	(7,1)		(7,3)		(7,5)		(7,7)		(7,9)		(7,11)	
0x08	(8,1)		(8,3)		(8,5)		(8,7)		(8,9)		(8,11)	
0x09	(9,1)		(9,3)		(9,5)		(9,7)		(9,9)		(9,11)	
0x0a	(10,1)		(10,3)		(10,5)		(10,7)		(10,9)		(10,11)	
0x0b	(11,1)		(11,3)		(11,5)		(11,7)		(11,9)		(11,11)	

↓

FIG. 9

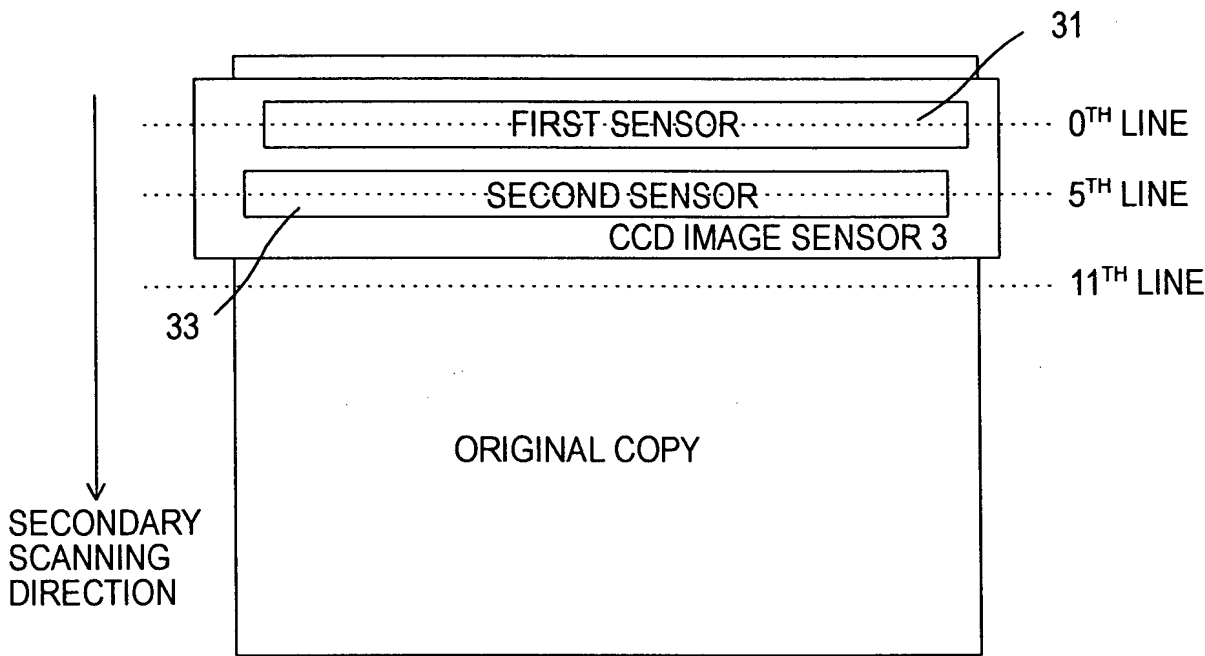


FIG. 10

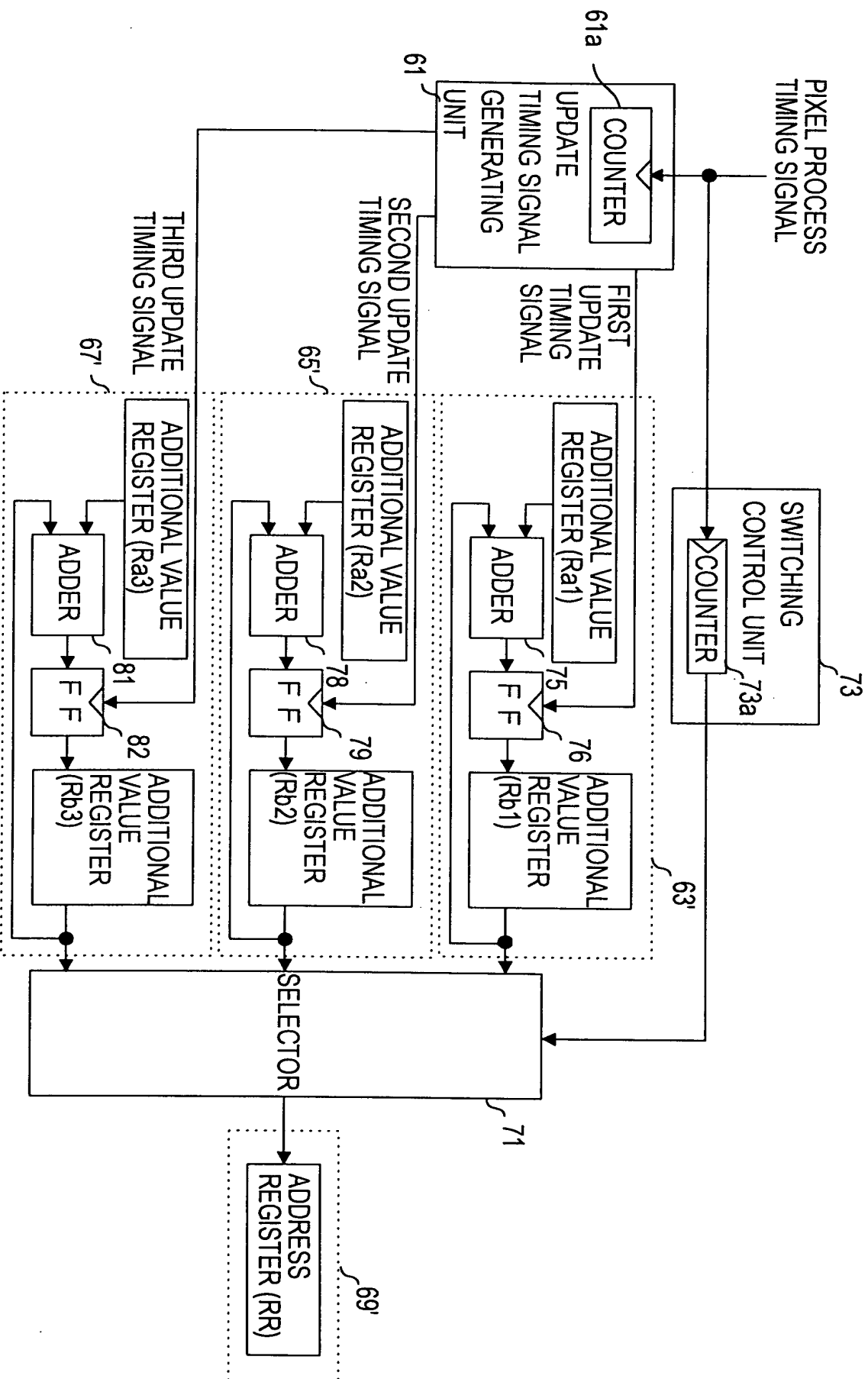


FIG. 11

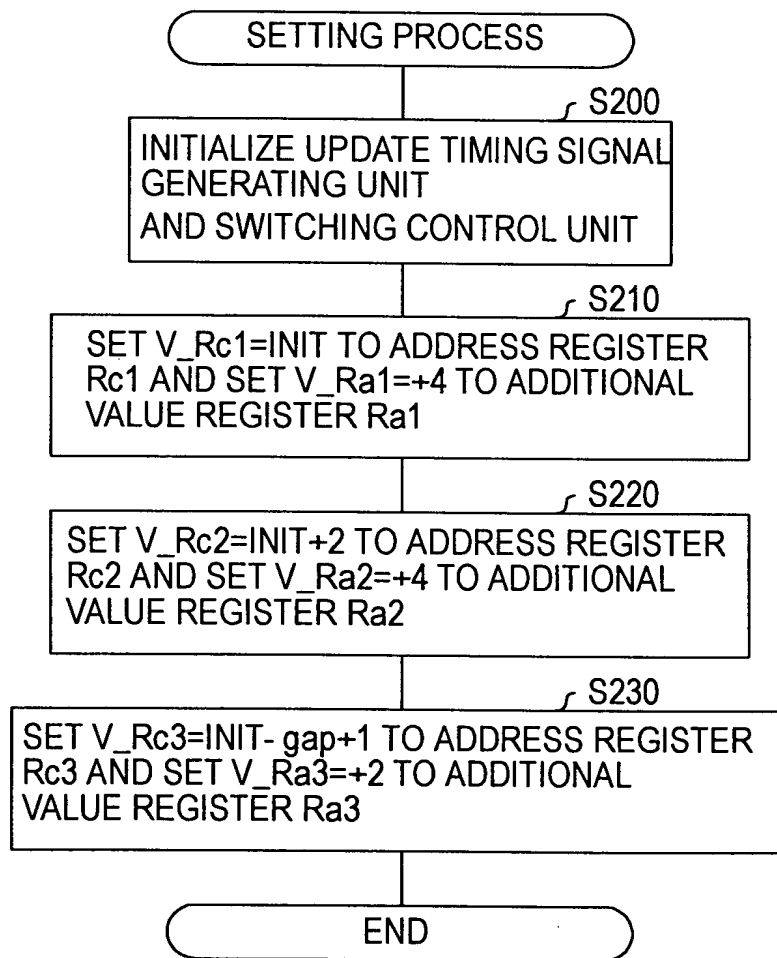


FIG. 12

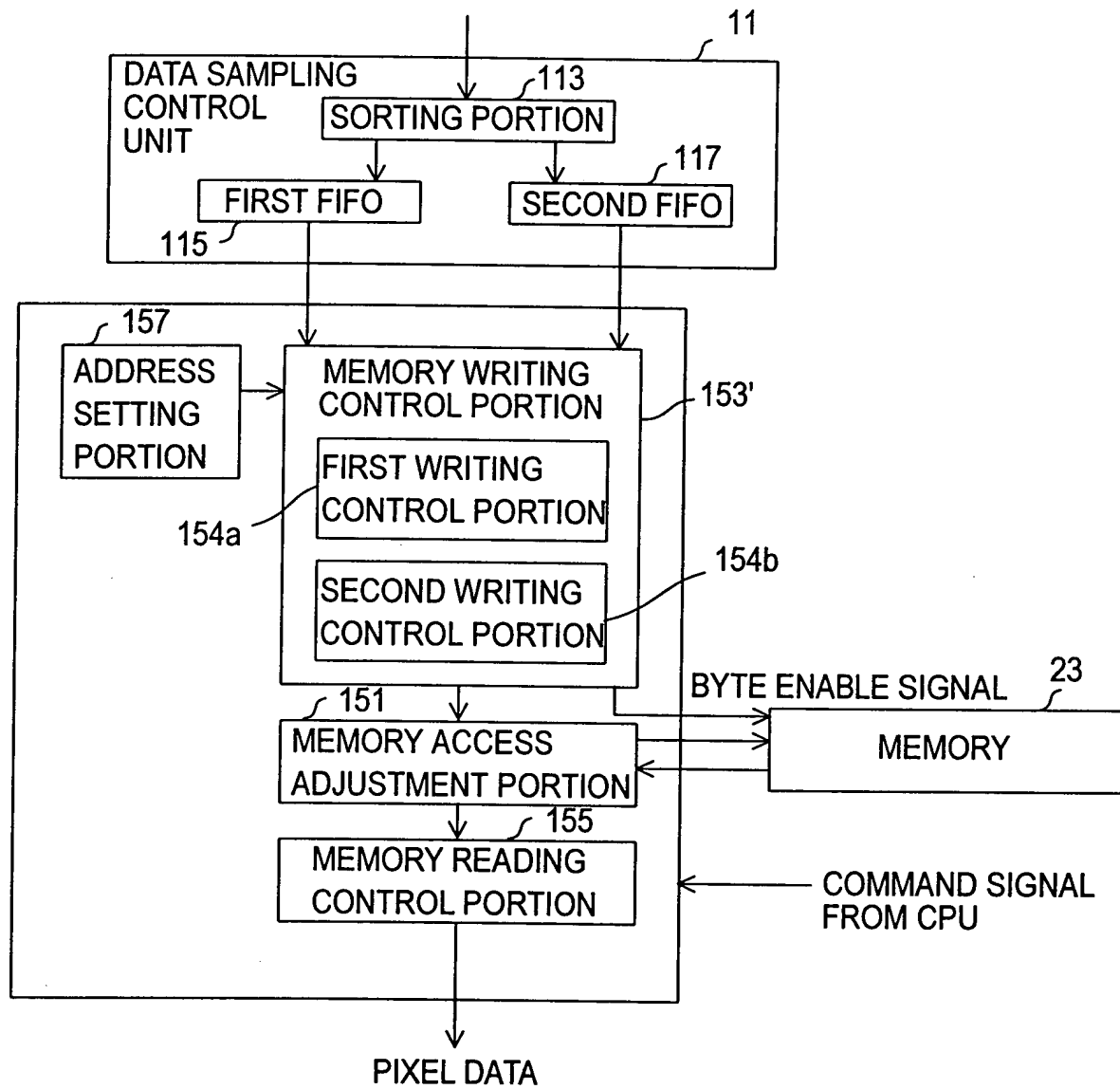


FIG. 13A

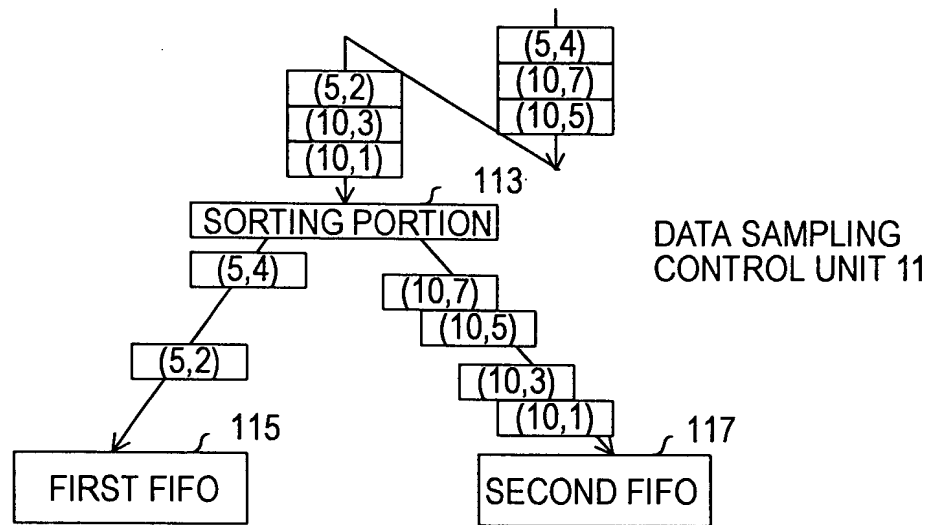


FIG. 13B

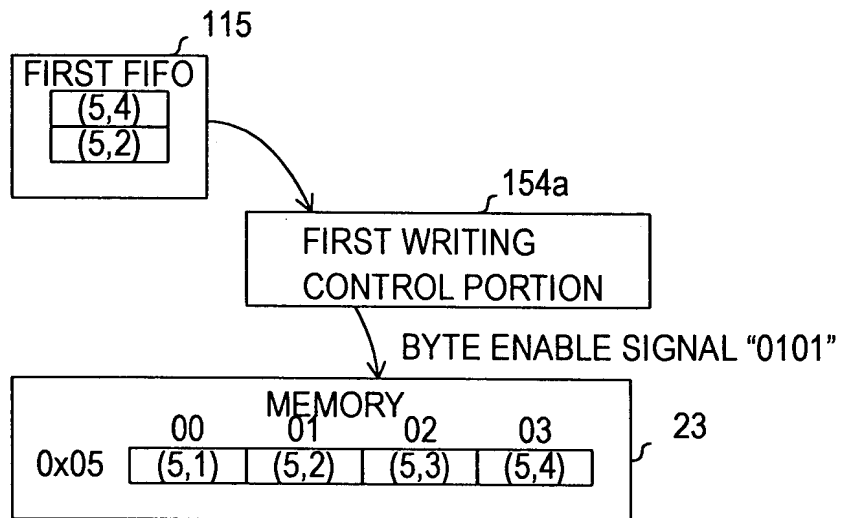


FIG. 13C

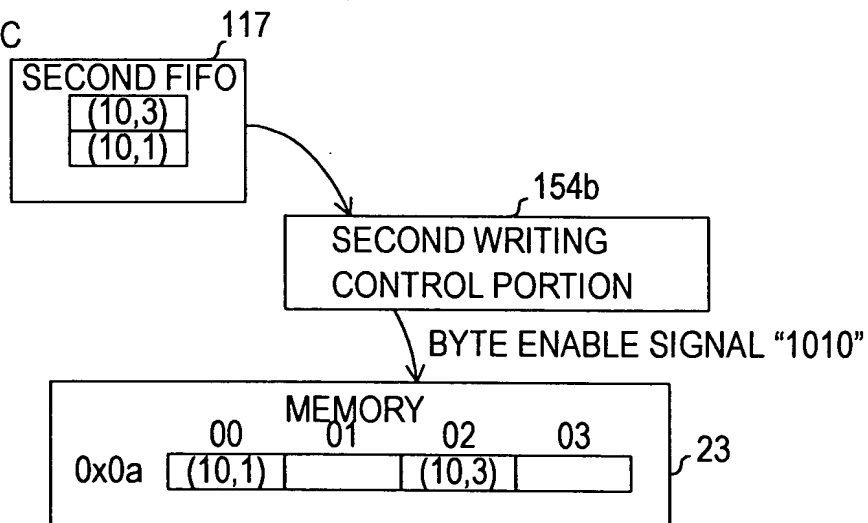


FIG. 14

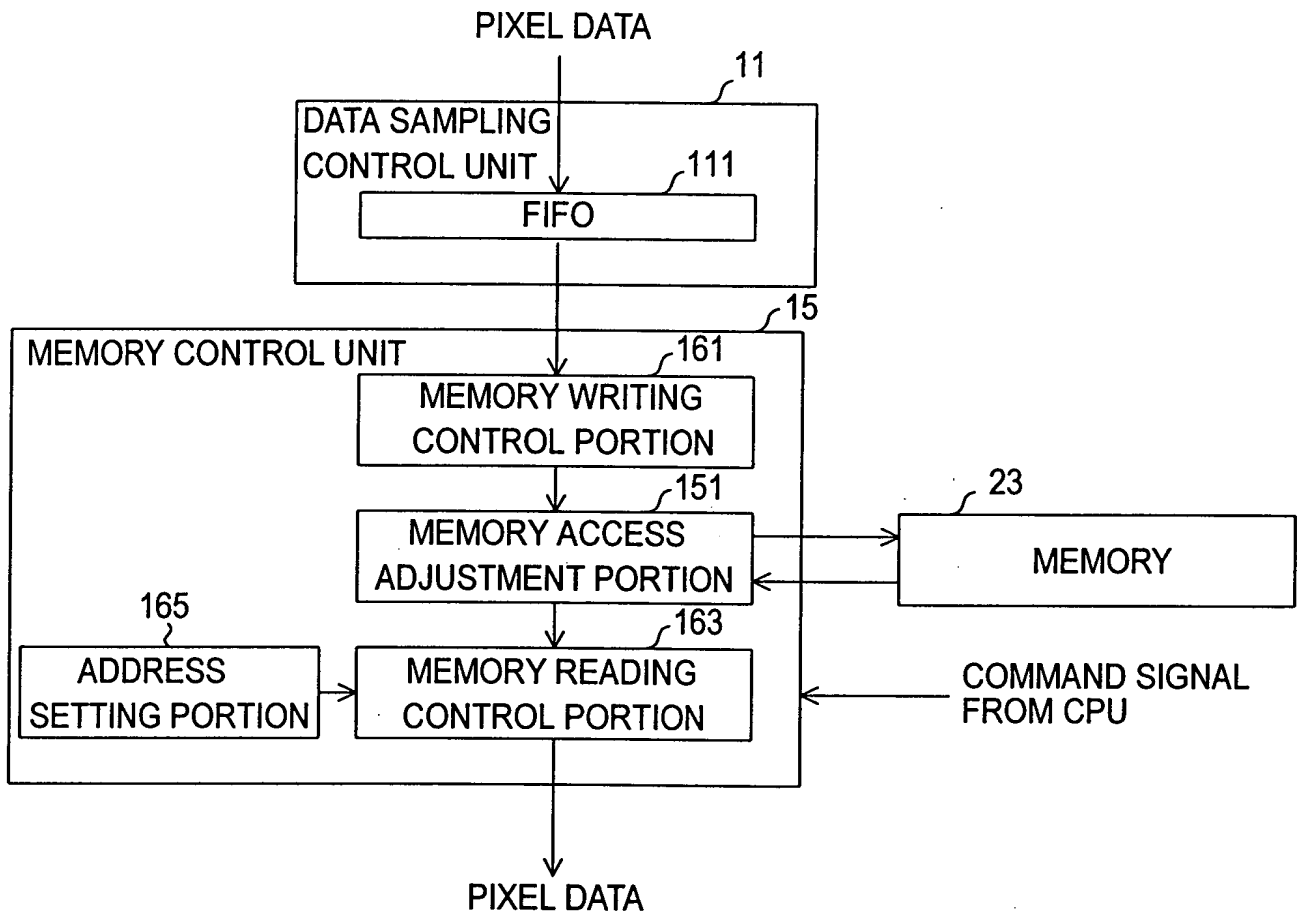


FIG. 15A

LOWER ADDRESS		HIGHER ADDRESS											
		00	01	02	03	04	05	06	07	08	09	0a	0b
0x05	(5,1)	(5,3)	(0,2)	(5,5)	(5,7)	(0,4)	(5,9)	(5,11)	(0,6)	(5,13)	(5,15)	(0,8)
0x06	(6,1)	(6,3)	(1,2)	(6,5)	(6,7)	(1,4)	(6,9)	(6,11)	(1,6)	(6,13)	(6,15)	(1,8)
0x07	(7,1)	(7,3)	(2,2)	(7,5)	(7,7)	(2,4)	(7,9)	(7,11)	(2,6)	(7,13)	(7,15)	(2,8)
0x08	(8,1)	(8,3)	(3,2)	(8,5)	(8,7)	(3,4)	(8,9)	(8,11)	(3,6)	(8,13)	(8,15)	(3,8)
0x09	(9,1)	(9,3)	(4,2)	(9,5)	(9,7)	(4,4)	(9,9)	(9,11)	(4,6)	(9,13)	(9,15)	(4,8)
0x0a	(10,1)	(10,3)	(5,2)	(10,5)	(10,7)	(5,4)	(10,9)	(10,11)	(5,6)	(10,13)	(10,15)	(5,8)
0x0b	(11,1)	(11,3)	(6,2)	(11,5)	(11,7)	(6,4)	(11,9)	(11,11)	(6,6)	(11,13)	(11,15)	(6,8)

FIG. 15B

HIGHER ADDRESS		LOWER ADDRESS											
		00	01	02	03	04	05	06	07	08	09	0a	0b
ADDRESS REGISTER Rc1	0x05-	00			03						09	
ADDRESS REGISTER Rc2	0x0a-		02									11
ADDRESS REGISTER Rc3	0x05-		01									
ADDRESS REGISTER RR		Rc1	Rc2	Rc3	Rc2	Rc1	Rc2	Rc3	Rc2	Rc1	Rc2	Rc3
READ-OUT PIXEL DATA		(5,1)	(5,2)	(5,3)	(5,4)	(5,5)	(5,6)	(5,7)	(5,8)	(5,9)	(5,10)	(5,11)

FIG. 16

